LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action upon Review
1.	05-12-2016	High pass, low pass RC circuits	I	CR		
2.	07-12-2016	Response of high pass and low pass RC circuit for sinusoidal	I	CR		
3.	08-12-2016	Step, pulse	I	CR		
4.	08-12-2016	Square and ramp inputs	I	CR		
5.	14-12-2016	RC circuit as differentiator, integrator	I	CR		
6.	15-12-2016	Attenuator	I	CR		
7.	15-12-2016	RL and RLC circuits	I	CR		
8.	19-12-2016	Response for step input	I	CR		
9.	21-12-2016	Ringing circuit	I	CR		
10.	22-12-2016	Problems	I	CR		
11.	22-12-2016	Diode clippers	II	CR		
12.	26-12-2016	Diode clippers	II	CR		
13.	28-12-2016	Diode clippers	II	CR		
14.	29-12-2016	Transistor clippers	II	CR		
15.	29-12-2016	clipping at two independent levels	II	CR		
16.	02-01-2017	Transfer characteristics of clippers	II	CR		
17.	04-01-2017	Emitter coupled clipper	II	CR		
18.	05-01-2017	Comparators, applications of voltage comparators	II	CR		
19.	05-01-2017	Clamping operation	II	CR		
20.	09-01-2017	Clamping circuits using diode with different inputs	II	CR		
21.	18-01-2017	Clamping circuits using diode with different inputs	II	CR		
22.	25-01-2017	Clamping circuit theorem	II	CR		
23.	30-01-2017	Practical clamping circuits	II	CR		
24.	01-02-2017	Effect of diode characteristics on clamping voltage	II	CR		
25.	02-02-2017	Transfer characteristics of clampers	II	CR		
26.	02-02-2017	Problems	II	CR		
27.	06-02-2017	Diode and transistor as switches	III	CR		
28.	08-02-2017	Break down voltage consideration of transistor	III	CR		
29.	09-02-2017	Saturation parameters of Transistor and their variation with temperature	III	CR		
30.	09-02-2017	Design of transistor switch	III	CR		

31.	13-02-2017	Transistor-switching times, Junction switching time	III	CR		
32.	15-02-2017	Analysis and design of Bistable Multivibrators	III	CR		
33.	16-02-2017	Fixed bias and self biased transistor binary circuits	III	CR		
34.	16-02-2017	Fixed bias and self biased transistor binary circuits	III	CR		
35.	20-02-2017	Commutating capacitors, triggering in binary	III	CR		
36.	22-02-2017	Schmitt trigger	III	CR		
37.	23-02-2017	Applications	III	CR		
38.	23-02-2017	Analysis and design of monostable multivibrator	IV	CR		
39.	27-02-2017	Collector-coupled monostable multivibrator	IV	CR		
40.	06-03-2017	Emitter-coupled monostable multivibrator	IV	CR		
41.	08-03-2017	Triggering in monostable multivibrator	IV	CR		
42.	09-03-2017	Analysis and design of astable multivibrator	IV	CR		
43.	09-03-2017	General features of a time base signal	IV	CR		
44.	15-03-2017	Methods of generating time base waveform	IV	CR		
45.	16-03-2017	Miller time base generator	IV	CR		
46.	16-03-2017	Bootstrap time base generator	IV	CR		
47.	20-03-2017	Transistor miller time base generator	IV	CR		
48.	22-03-2017	Transistor Bootstrap time base generator	IV	CR		
49.	23-03-2017	Monostable blocking oscillator Base timing	V	CR		
50.	23-03-2017	Emitter timing	V	CR		
51.	27-03-2017	Astable blocking oscillator diode controlled	V	CR		
52.	29-03-2017	RC controlled, Applications	V	CR		
53.	30-03-2017	Basic operating principles of sampling gates	V	CR		
54.	30-03-2017	Unidirectional sampling gates	V	CR		
55.	03-04-2017	Bi-directional sampling gates	V	CR		
56.	06-04-2017	Bi-directional sampling gates	V	CR		
57.	06-04-2017	Reduction of pedestal in Gate circuits	V	CR		
58.	10-04-2017	Four diode sampling gates, Applications sampling gates	V	CR		

Faculty: Faculty-incharge: H.O.D.